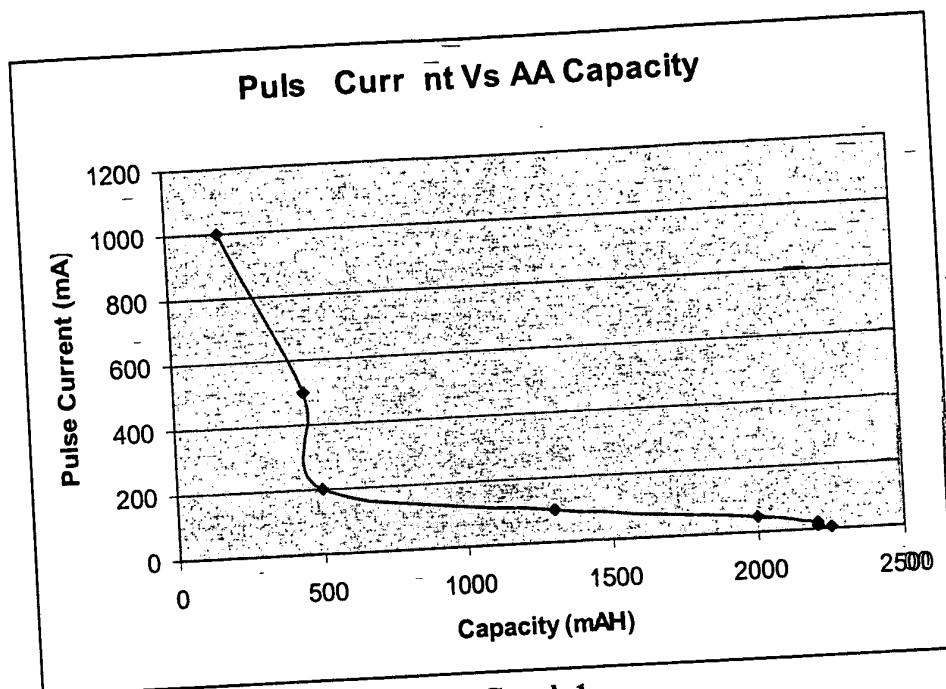
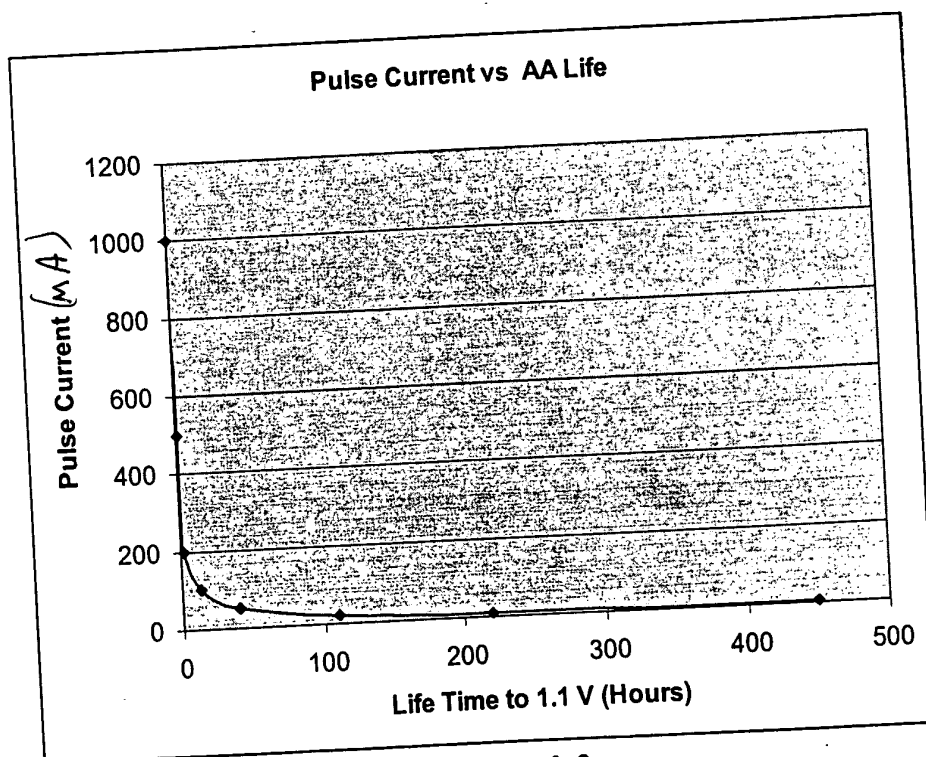


Figure 1



Graph 1

Figure 2.



Graph 2

Figure 3.

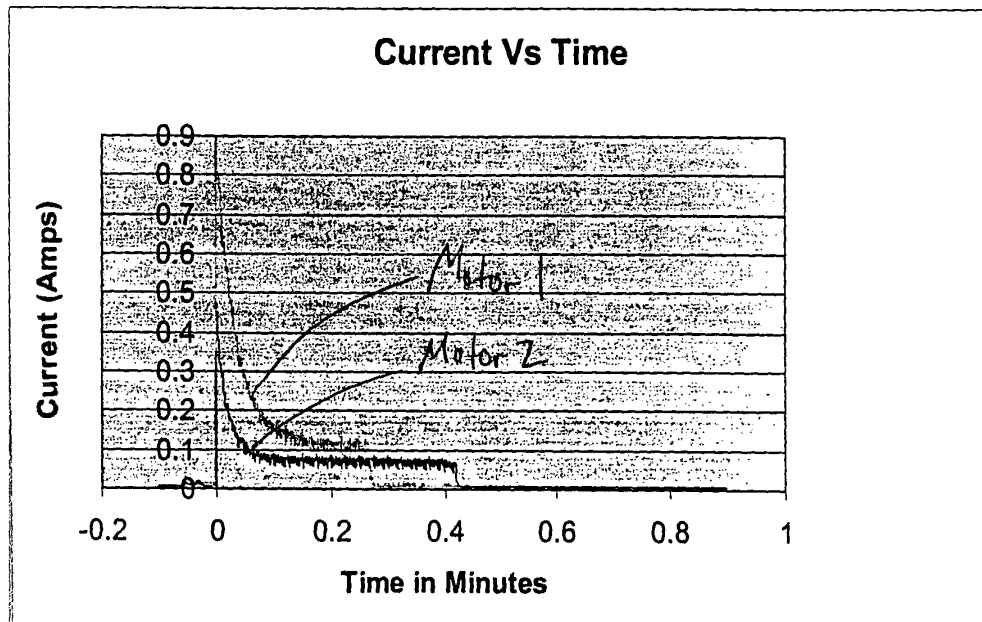


Figure 4

Graph 3

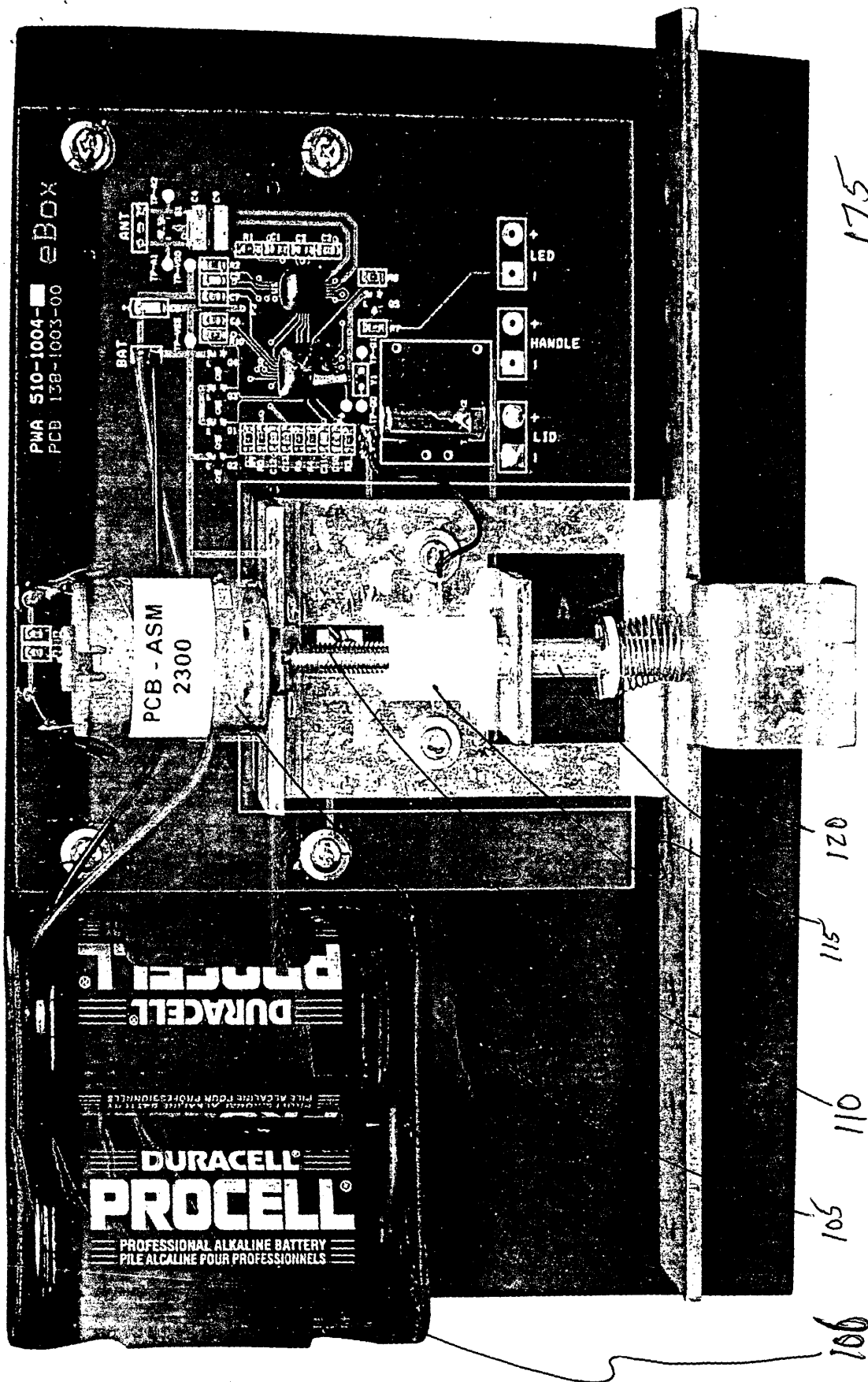


Figure 5A

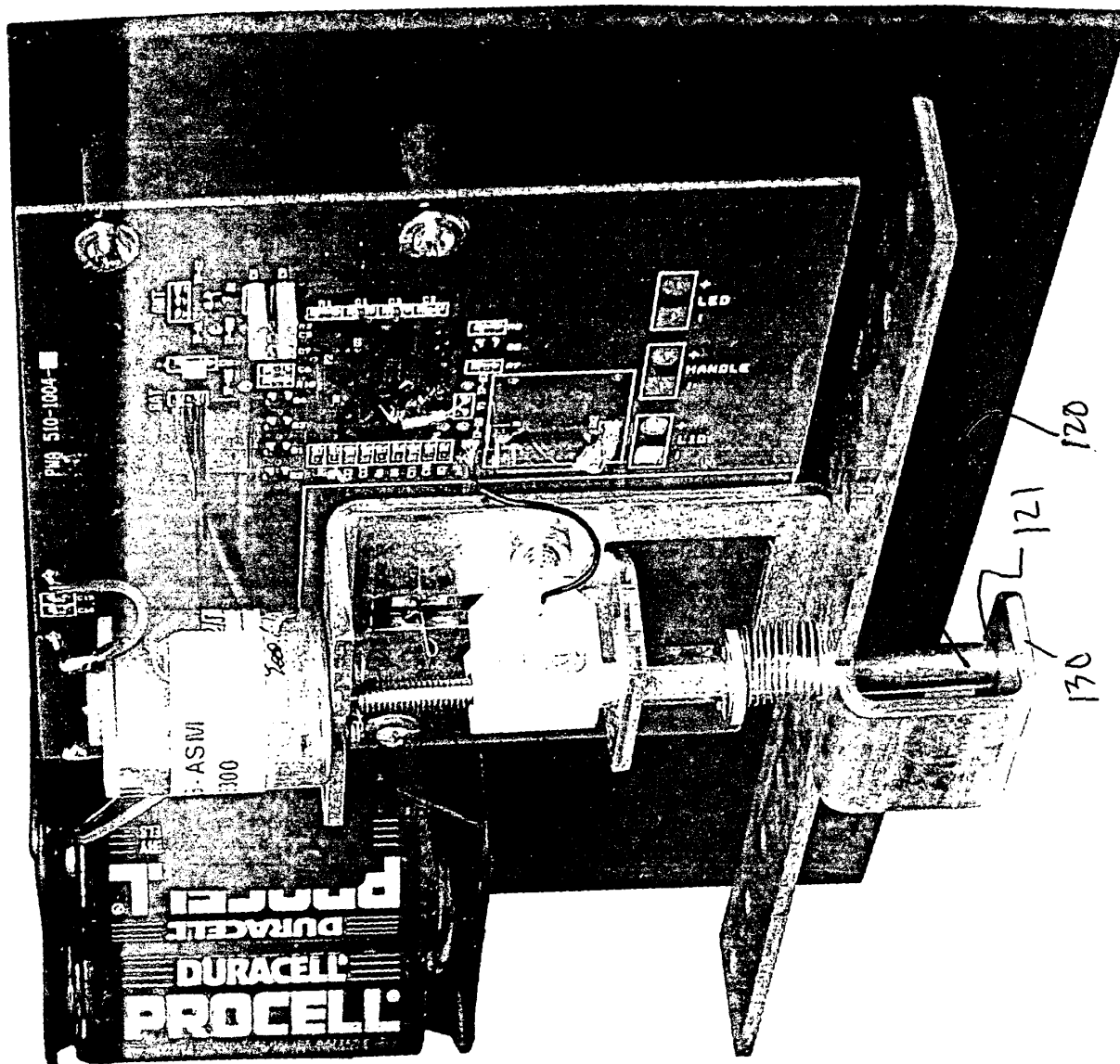


Figure 5B

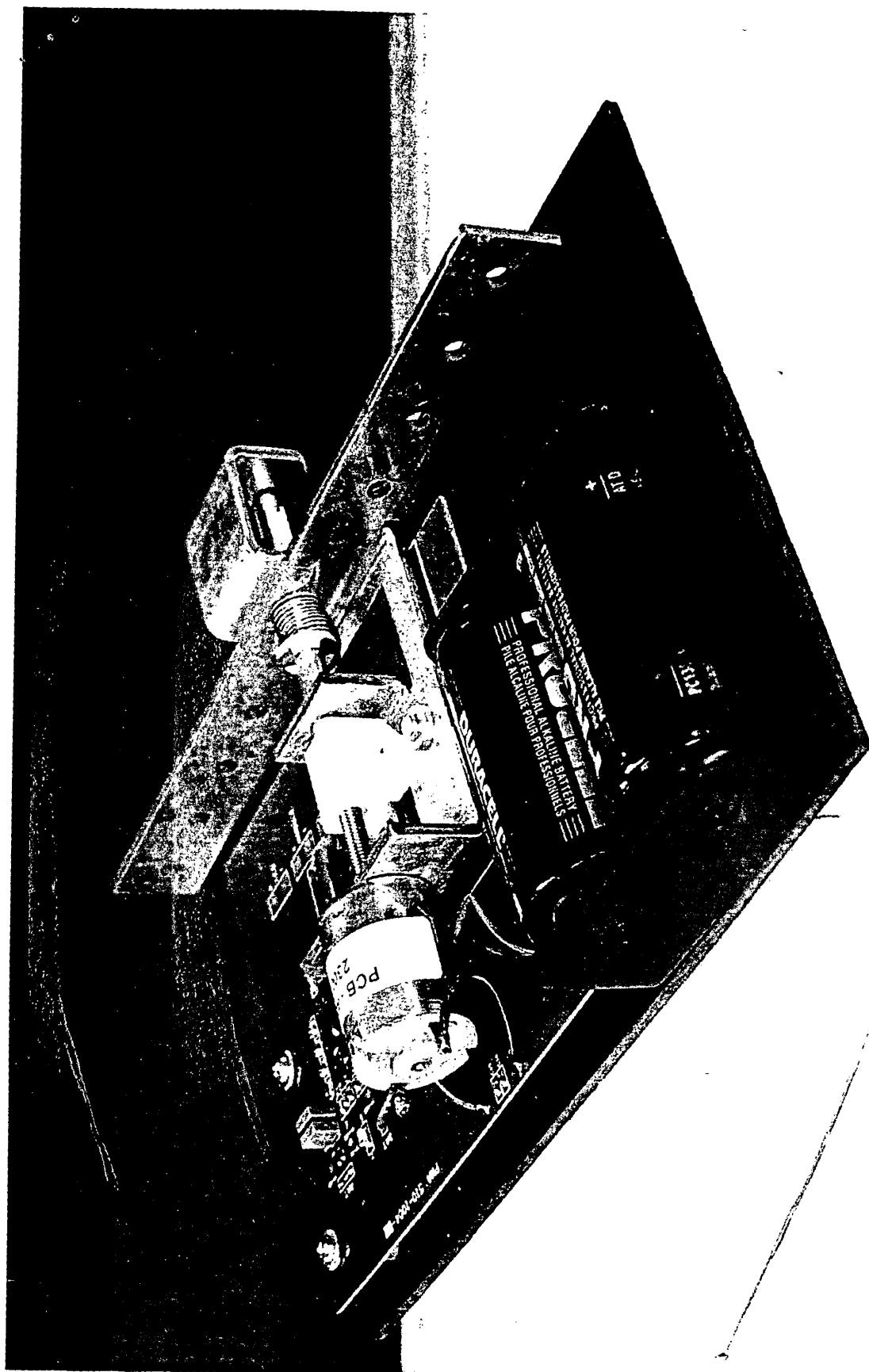


Figure 5C

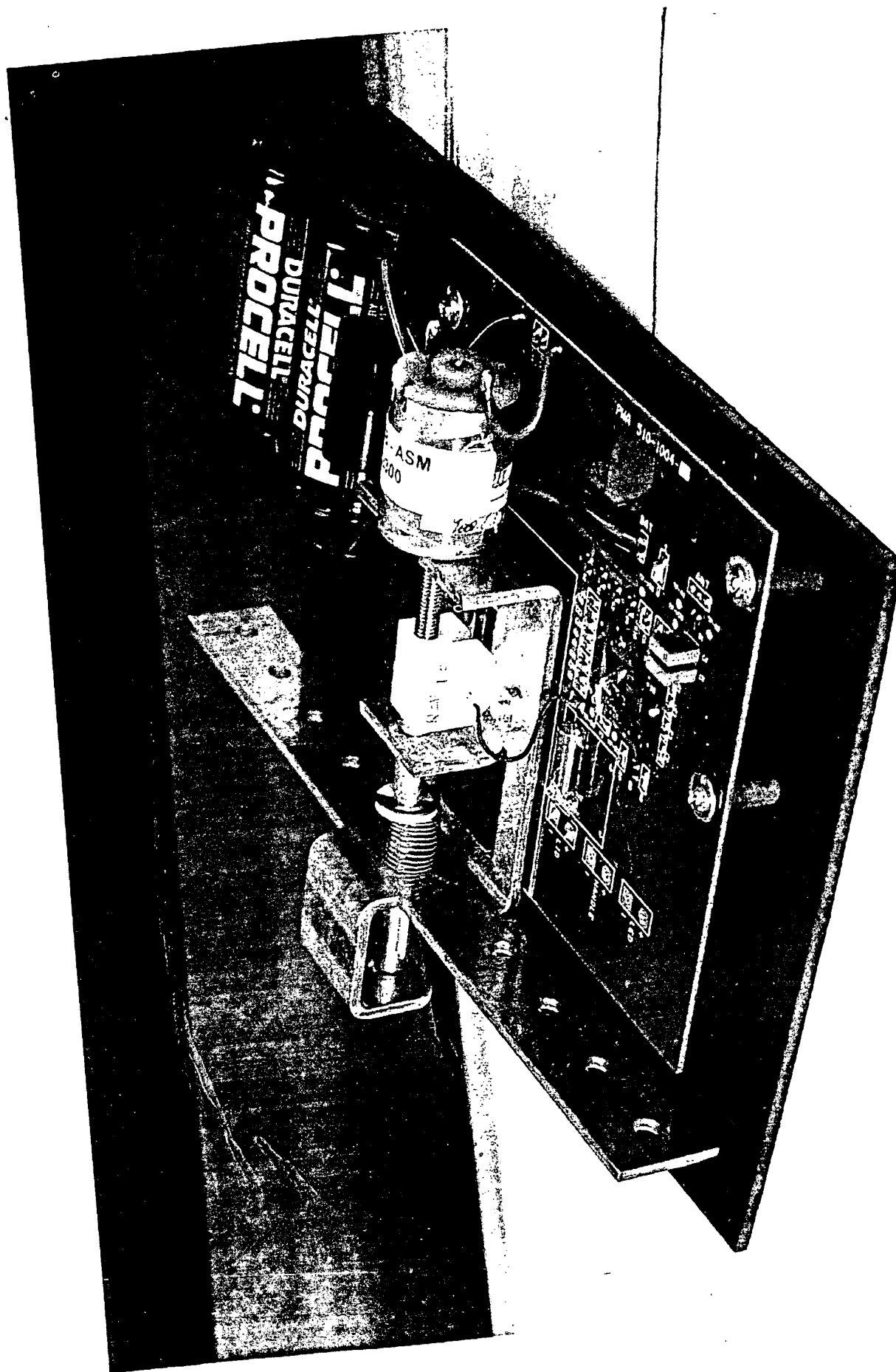


Figure 5D

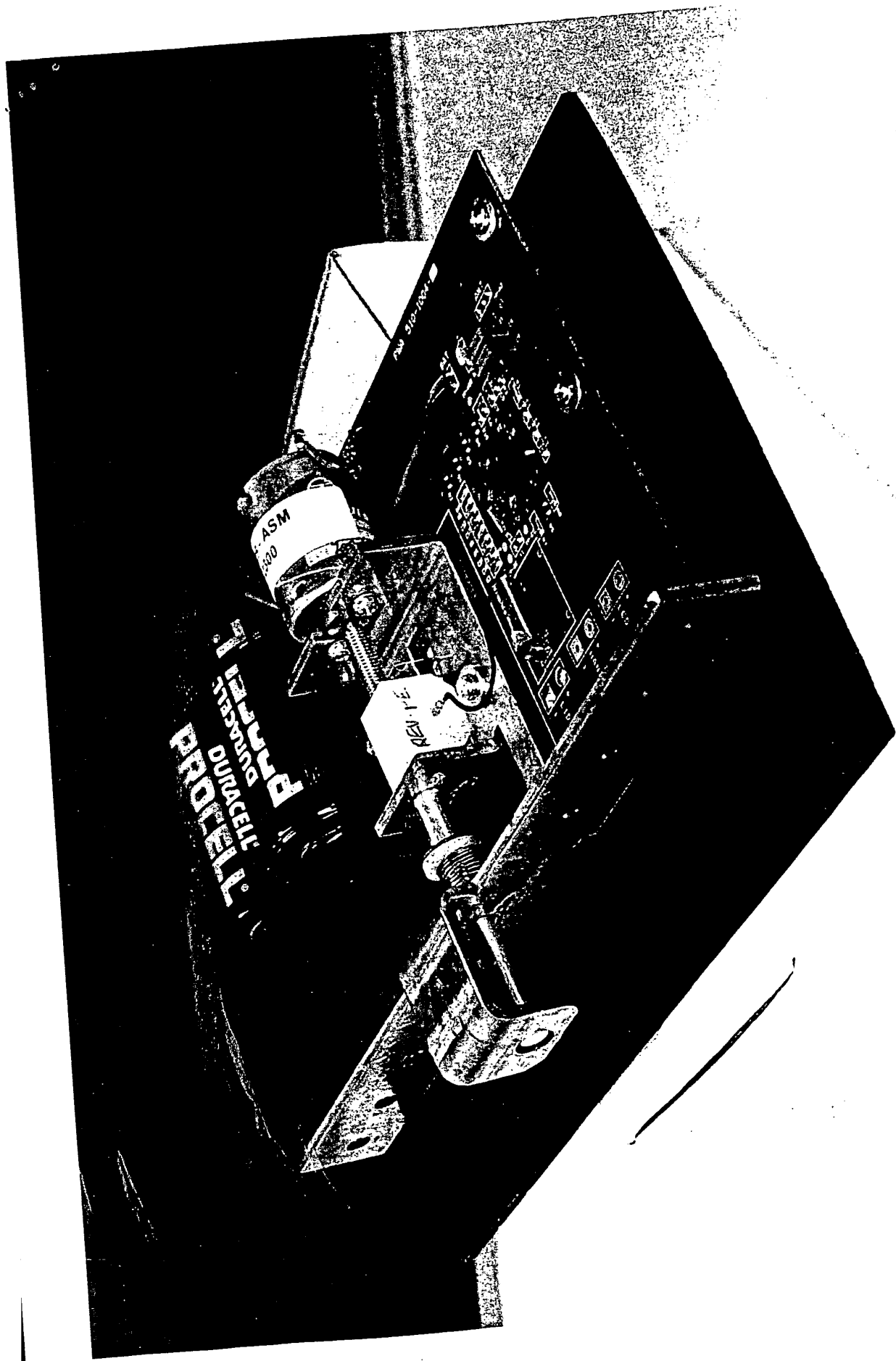


Figure SE

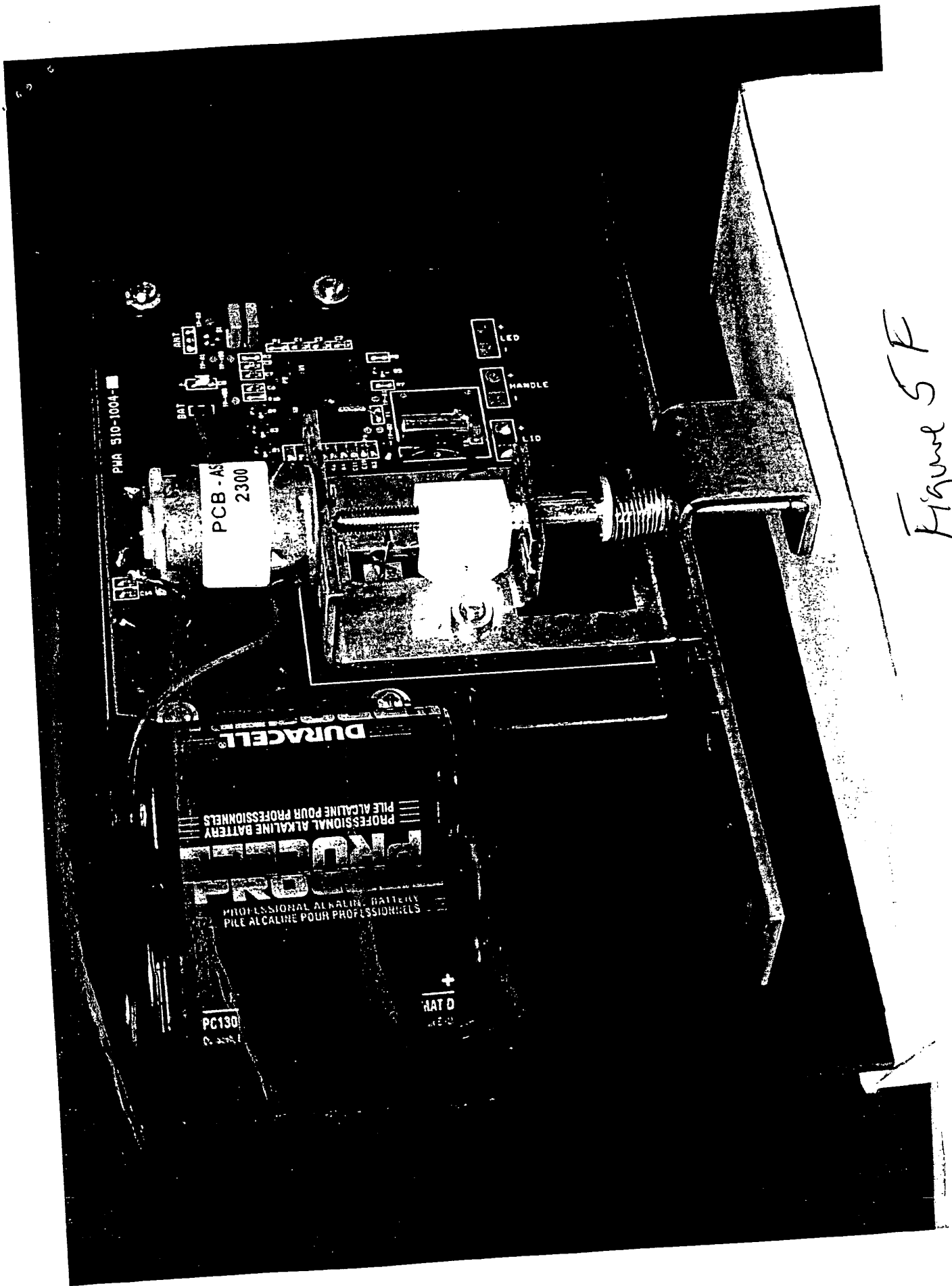
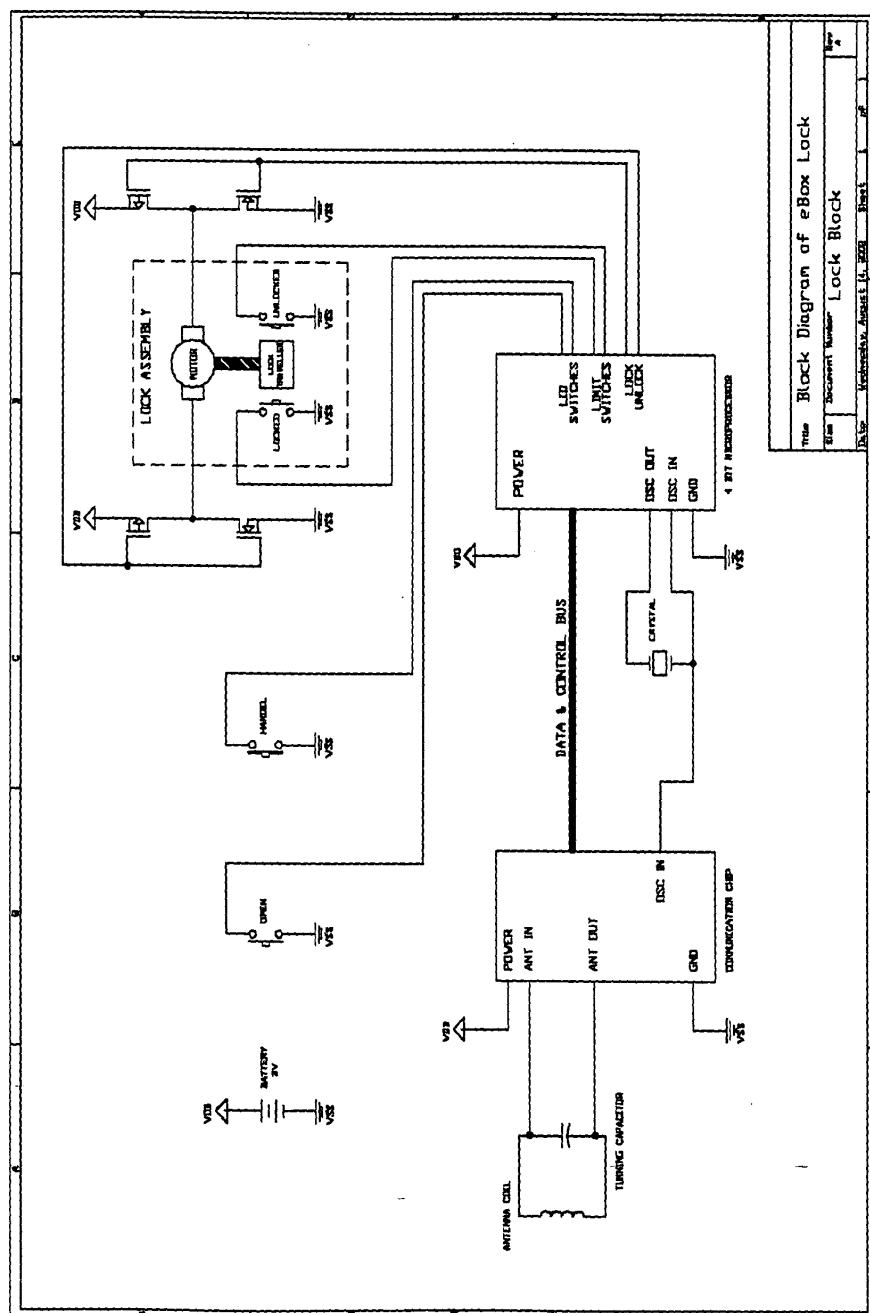


Figure 5F



Title	Block Diagram of eBox Lock
Rev	Document Number
Rev	Lock Block
Rev	Author
Rev	Checked
Rev	Approved

Figure 6

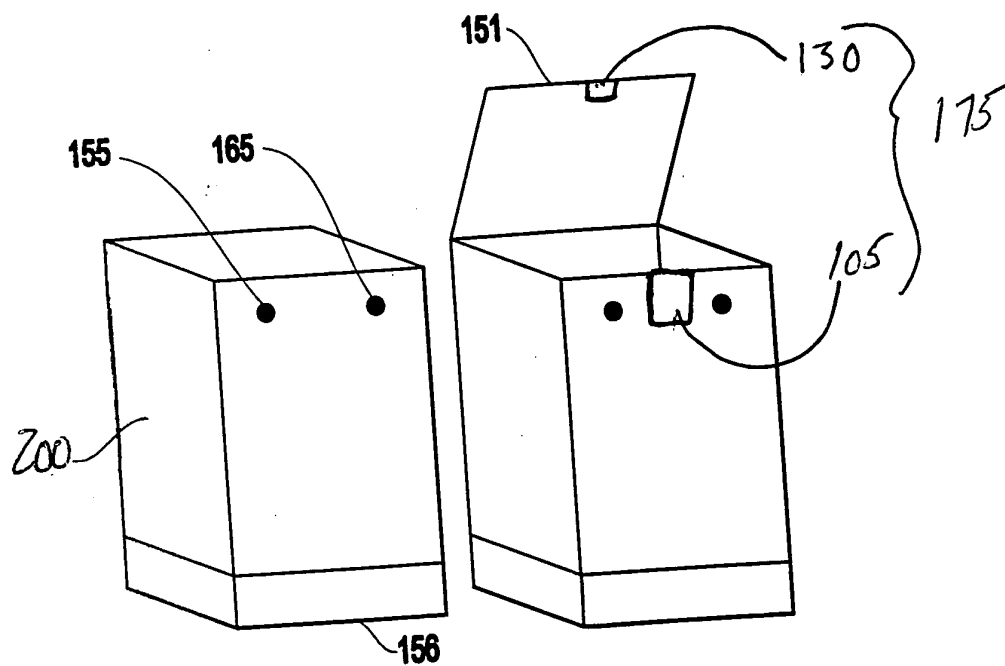


FIG. 1A

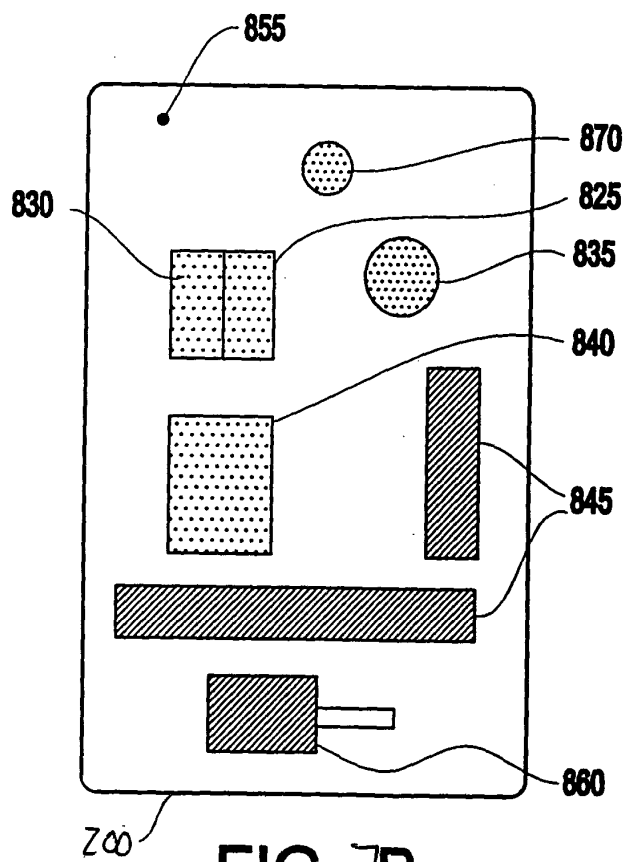


FIG. 7B

700

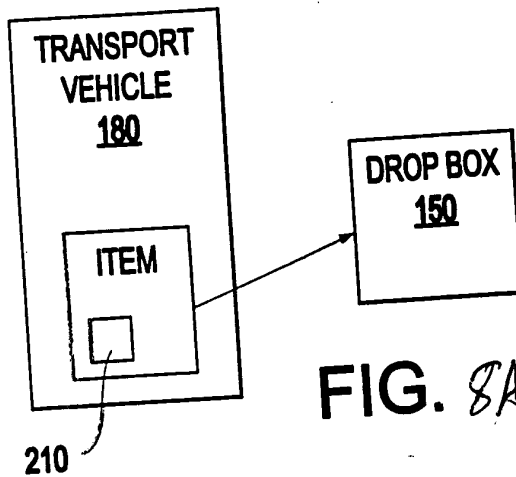


FIG. 8A

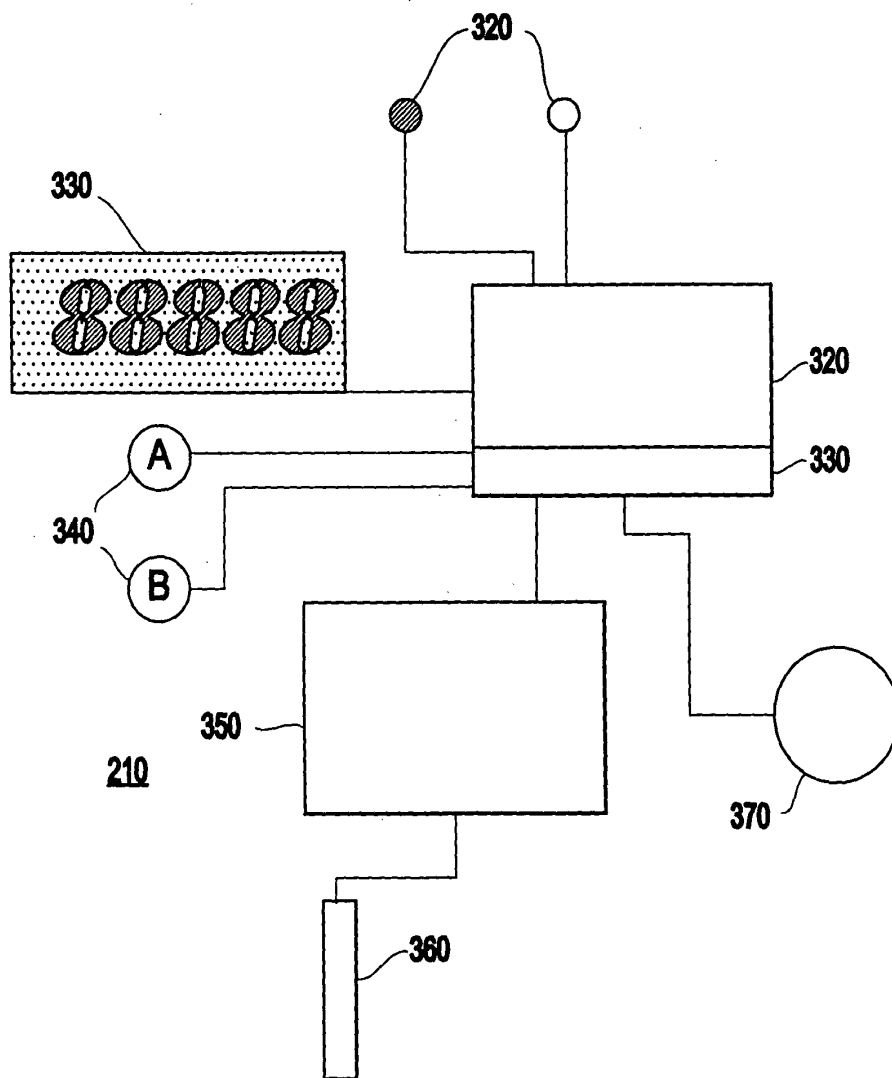


FIG. 8B

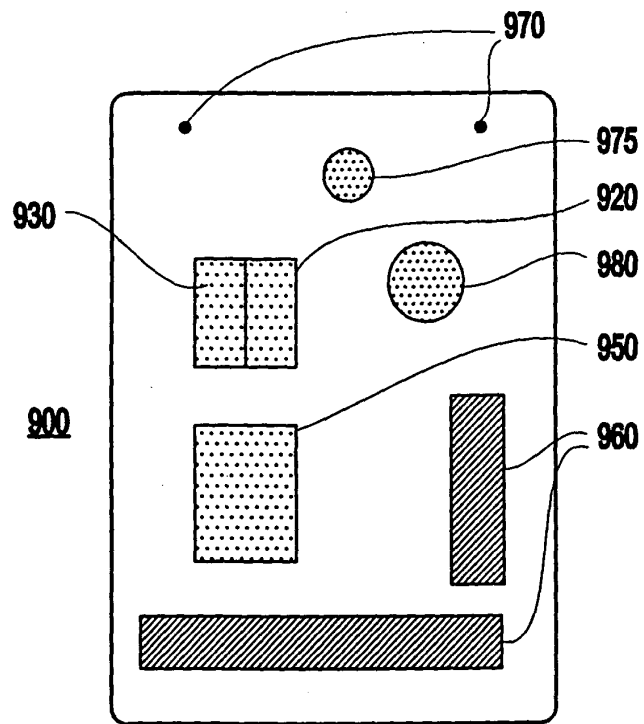


FIG. 9A

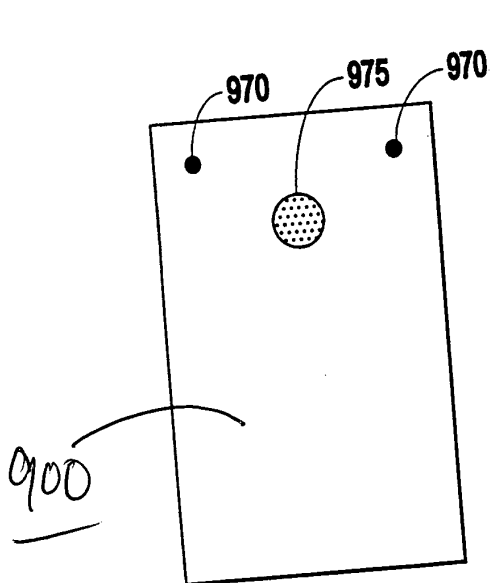


FIG. 9B

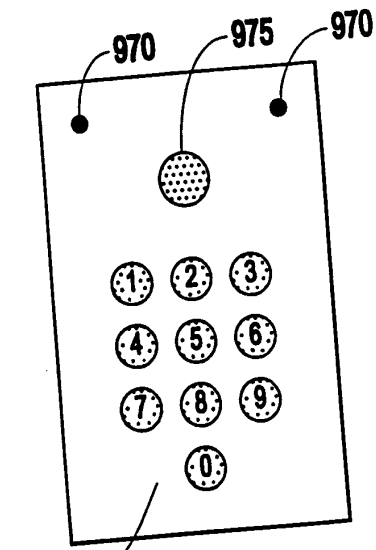
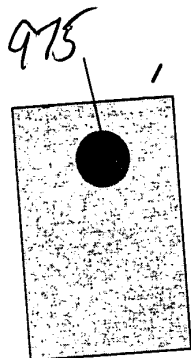


FIG. 9C

Wireless signal



900

Communications
Chip

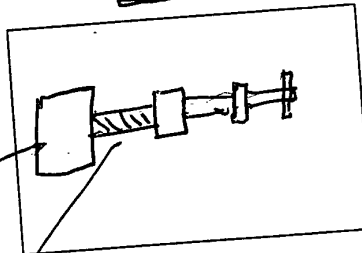
176

Four Bit
Microprocessor
RAM

177



178



105

Figure 10

110

175

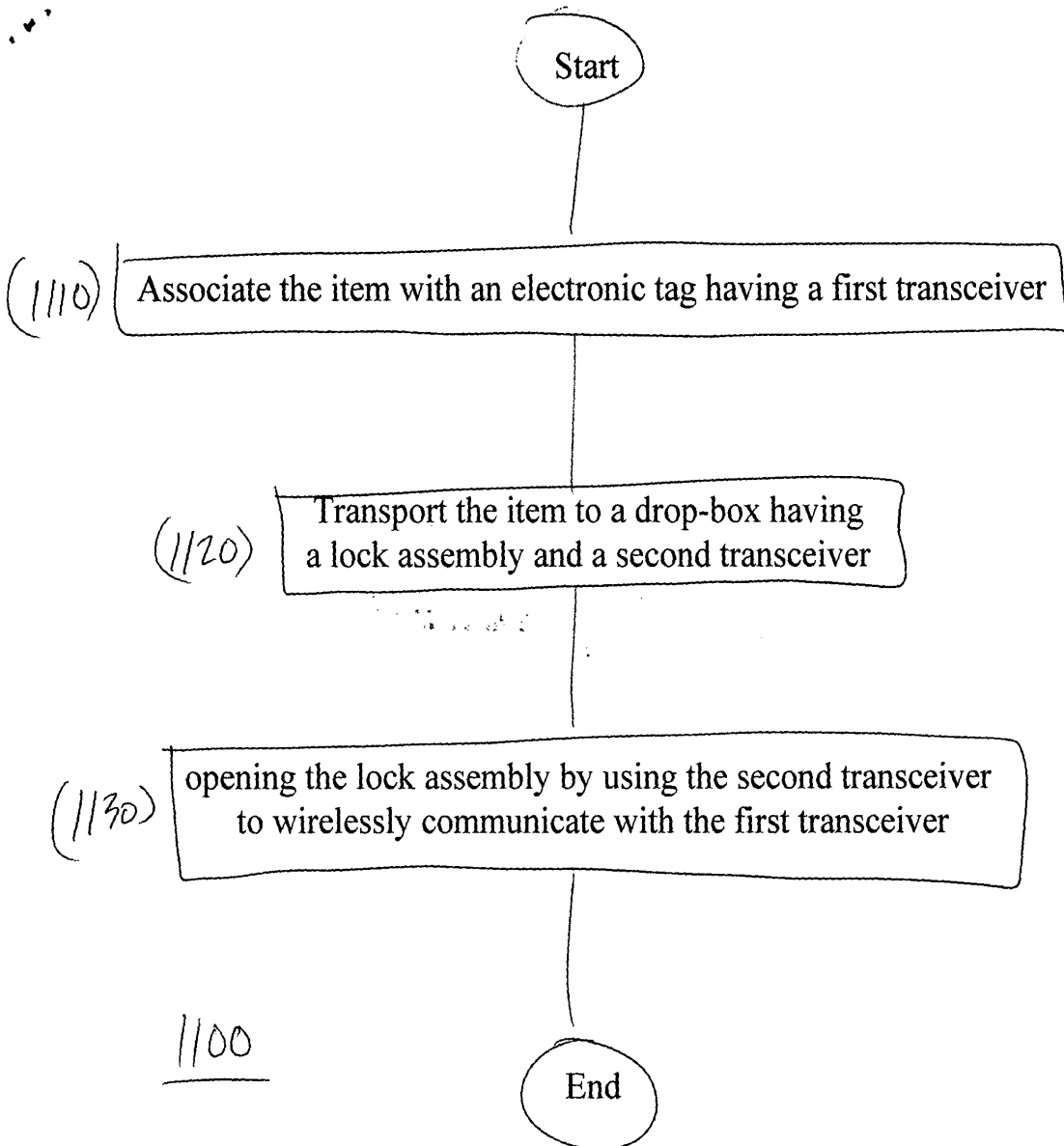


Figure 11